

a common auxiliary electrode on a layer equal to the gate lines to surround the pixel region;

a gate insulating film on the first substrate;

a passivation film on the gate insulating film including the first substrate;

a pixel electrode in the pixel region, the pixel electrode not overlapping the common auxiliary electrode;

a light-shielding layer on the second substrate;

a color filter layer on the light-shielding layer;

a common electrode on the color filter layer;

a plurality of electric field distortion dielectric structures patterned in different forms within neighboring pixels; and

an alignment film on at least one of the first and second substrates.

12. (Amended) A multi-domain liquid crystal display device comprising:
- first and second substrates opposing each other between a liquid crystal layer;
- a plurality of gate bus lines and data lines on the first substrate lengthwise and crosswise, to define a pixel region;
- a common auxiliary electrode on a layer equal to the gate lines to surround the pixel region;
- a gate insulating film on the first substrate;
- a passivation film on the gate insulating film including the first substrate, the pixel electrode having an electric field induction window therein;
- a pixel electrode in the pixel region;

- a light-shielding layer on the second substrate;
- a color filter layer on the light-shielding layer;
- a common electrode on the color filter layer;
- a plurality of electric field distortion dielectric structures patterned in different forms within neighboring pixels; and
- an alignment film on at least one of the first and second substrates.

13. (Amended) A multi-domain liquid crystal display device comprising:

- first and second substrates opposing each other between a liquid crystal layer;
- a plurality of gate bus lines and data lines on the first substrate lengthwise and crosswise, to define a pixel region;
- a common auxiliary electrode on a layer equal to the gate lines to surround the pixel region;
- a gate insulating film on the first substrate;
- a passivation film on the gate insulating film including the first substrate, the passivation film having an electric field induction window therein;
- a pixel electrode in the pixel region;
- a light-shielding layer on the second substrate;
- a color filter layer on the light-shielding layer;
- a common electrode on the color filter layer;
- a plurality of electric field distortion dielectric structures patterned in different forms within neighboring pixels; and
- an alignment film on at least one of the first and second substrates.

14. (Amended) A multi-domain liquid crystal display device comprising:
first and second substrates opposing each other between a liquid crystal layer;
a plurality of gate bus lines and data lines on the first substrate lengthwise and crosswise, to define a pixel region;
a common auxiliary electrode on a layer equal to the gate lines to surround the pixel region;
a gate insulating film on the first substrate, the gate insulating film having an electric field induction window therein;
a passivation film on the gate insulating film including the first substrate;
a pixel electrode in the pixel region;
a light-shielding layer on the second substrate;
a color filter layer on the light-shielding layer;
a common electrode on the color filter layer;
a plurality of electric field distortion dielectric structures patterned in different forms within neighboring pixels; and
an alignment film on at least one of the first and second substrates.

15. (Amended) A multi-domain liquid crystal display device comprising:
first and second substrates opposing each other between a liquid crystal layer;
a plurality of gate bus lines and data lines on the first substrate lengthwise and crosswise, to define a pixel region;

a common auxiliary electrode on a layer equal to the gate lines to surround the pixel region;

a gate insulating film on the first substrate;

a passivation film on the gate insulating film including the first substrate;

a pixel electrode in the pixel region;

a light-shielding layer on the second substrate;

a color filter layer on the light-shielding layer;

a common electrode on the color filter layer, the common electrode having an electric field induction window therein;

a plurality of electric field distortion dielectric structures patterned in different forms within neighboring pixels; and

an alignment film on at least one of the first and second substrates.

16. (Amended) A multi-domain liquid crystal display device comprising:
first and second substrates opposing each other between a liquid crystal layer;
a plurality of gate bus lines and data lines on the first substrate lengthwise and crosswise, to define a pixel region;

a common auxiliary electrode on a layer equal to the gate lines to surround the pixel region;

a gate insulating film on the first substrate;

a passivation film on the gate insulating film including the first substrate;

a pixel electrode in the pixel region;

a light-shielding layer on the second substrate;

a color filter layer on the light-shielding layer, the color filter layer having an electric field induction window therein;

a common electrode on the color filter layer;

a plurality of electric field distortion dielectric structures patterned in different forms within neighboring pixels; and

an alignment film on at least one of the first and second substrates.

18. (Amended) A multi-domain liquid crystal display device comprising:
first and second substrates opposing each other between a liquid crystal layer;
a plurality of gate bus lines and data lines on the first substrate lengthwise and crosswise, to define a pixel region;

a common auxiliary electrode on a layer equal to the gate lines to surround the pixel region;

a gate insulating film on the first substrate;

a passivation film on the gate insulating film including the first substrate;

a pixel electrode in the pixel region;

a light-shielding layer on the second substrate;

a color filter layer on the light-shielding layer;

an over coat layer on the color filter layer, the over coat layer having an electric field induction window therein;

a common electrode on the over coat layer;

a plurality of electric field distortion dielectric structures patterned in different forms within neighboring pixels; and